



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: KHAN et al.

Serial No. : 10/618,514

Group Art Unit: 2823

Filed : 07/11/2003

Examiner: KEBEDE, B.

For : METHOD OF FABRICATING SEMICONDUCTOR DEVICE
HAVING TRIPLE LDD STRUCTURE AND LOWER GATE
RESISTANCE FORMED WITH A SINGLE IMPLANT PROCESS

AMENDMENT AND RESPONSE AFTER FINAL ACTION

Mail Stop AF
Commissioner for Patents
P.O Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action mailed 12/28/2004, please enter and consider the following amendments and arguments for the above captioned patent application.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.

3/15/05

(Bn)

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